

INVENTOR INFORMATION

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CORRESPONDENCE INFORMATION

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APPLICATION INFORMATION

Title Line One:: MULTILAYER CIRCUIT COMPONENT AND METHOD
Title Line Two:: FOR MANUFACTURING THE SAME
Total Drawing Sheets:: 3
Formal Drawings?: Yes
Application Type:: Utility
Docket Number:: P/1071-1380
Secrecy Order in Parent Appl.?: No

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 2000-180086
Filing Date:: 06-15-2000
Country:: Japan
Priority Claimed:: Yes
Foreign Application Two:: 2000-206262
Filing Date:: 07-07-2000
Country:: Japan
Priority Claimed:: Yes

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